



INFORMATION DISCLOSURE CITATION

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US Nat'l of PCT/IL98/00476

Gavriel J. IDDAN, et al.

09/806252

FILING
HerewithGROUP
To be assigned 2512

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
LN	1	5,329,312	12 Jul '94	BOISVERT, D. M. et al.			
LN	2	5,488,415	30 Jan '96	UNO, M.			
LN	3	5,081,530	14 Jan '92	MEDINA, A.			
LN	4	4,935,616	19 Jun '90	SCOTT, M. W.			
LN	5	5,345,266	6 Sep '94	DENYER, P. B.			
LN	6	4,689,487	25 Aug '87	NISHIKI, M. et al.			
LN	7	5,574,765	12 Nov '96	HASSLER, D. et al.			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
LN	8	WO 97/28558	7 Aug '97	PCT				
LN	9	WO 97/01111	9 Jan '97	PCT				
LN	10	WO 97/01112	9 Jan '97	PCT				
LN	11	WO 97/01113	9 Jan '97	PCT				
LN	12	WO 99/40478	12 Aug '99	PCT				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

LN	13	CHRISTIE, S. et al.; "Design and Development of a Multi-Detecting Two-Dimensional Ranging Sensor"; Measurement Science and Technology; Vol. 6; pp. 1301-1308; 1995						
LN	14	YATES, G. J. et al.; "Range-Gated Imaging for Near Field Target Identification"; SPIE; Vol. 2869; pp. 374-385						

EXAMINER

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DATE CONSIDERED

6/2/05

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INFORMATION DISCLOSURE CITATION

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Docket Number (Optional)

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Application Number

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Gavriel J. IDDAN, et al.

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Group Art Unit

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LN

15

DENYER, P.; "Design and Use of Passive and Active Pixel CMOS Cameras"; SSCTC Workshop on CMOS Imaging Technology; pp. 1-18; February 7, 1996

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16

VIETZ, O. et al.; "Image Sensing with Programmable Offset Pixels for Increased Dynamic Range of more than 150 dB"; SPIE; Vol. 2654; pp. 93-98

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HEANUE, J. A. et al.; "CMOS Detector Readout Electronics for an Emission-Transmission Medical Imaging System"; IEEE Transactions on Nuclear Science; Vol. 42, No. 4; pp. 1133-1138; August 1995

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SCHWARTE, R. et al.; "A New Electrooptical Mixing and Correlating Sensor: Facilities and Applications of the Photonic Mixer Device (PMD)"; SPIE; Vol. 3100; pp. 245-253

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DECKER, S. et al.; "FPN Reduction Techniques in CMOS Imaging Arrays"; SSCTC Workshop on CMOS Imaging Technology; February 7, 1996

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